RAM **BOOT MEMORY** 64 63 62 BOOT AREA DESIGNATION FLAG B BOOT AREA DESIGNATION FLAG D BOOT AREA DESIGNATION FLAG C BOOT AREA DESIGNATION FLAG A INSTRUCTION FETCH BUS NONVOLATILE MEMORY USER REGION USER REGION USER REGION USER REGION DATA BUS FIG. 61 ADDRESS SIGNAL REGION SWITCHING FLAG CPU

FIG.2A

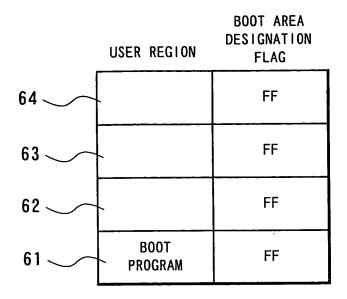


FIG.2B

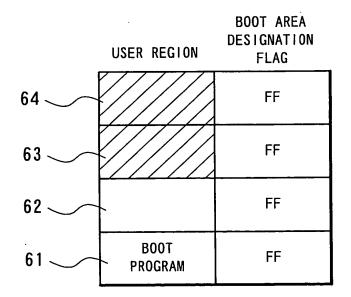


FIG.2C

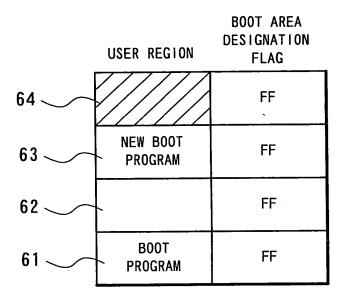


FIG.2D

	USER REGION	BOOT AREA DESIGNATION FLAG
64		FF
63	NEW BOOT PROGRAM	FE
62		FF
61	BOOT PROGRAM	FF

FIG.2E

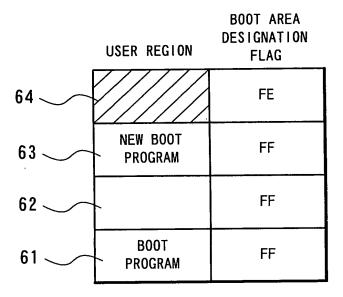
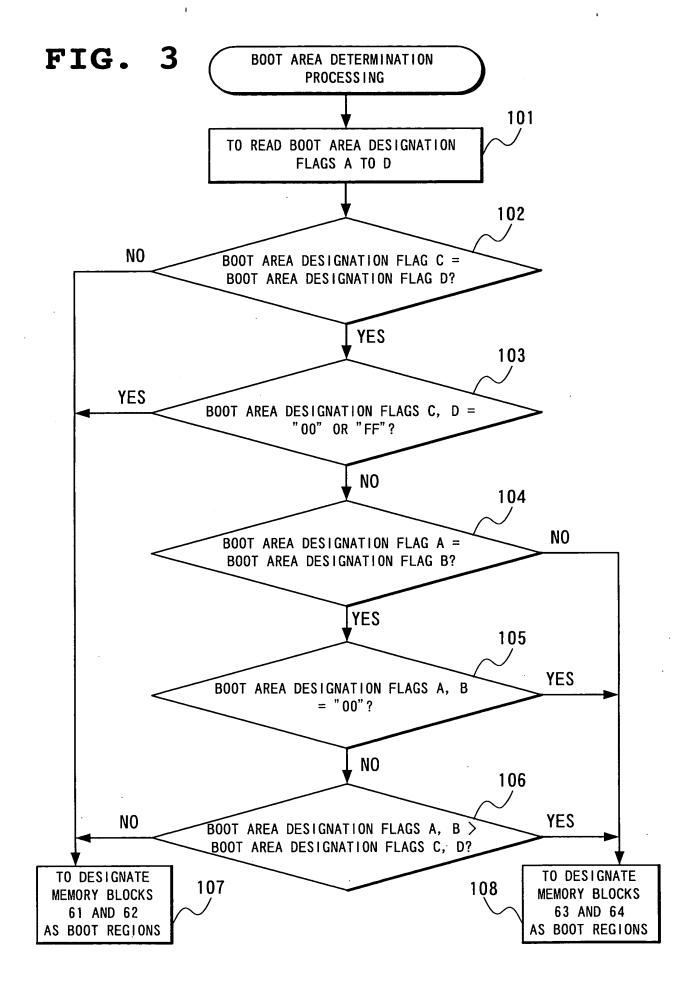


FIG.2F

	USER REGION	BOOT AREA DESIGNATION FLAG
64		FF
63	NEW BOOT PROGRAM	FE
62		FF
61		FF



RAM **BOOT MEMORY** 162 161 FIG. 4 (PRIOR ART) BOOT AREA DESIGNATION FLAG B BOOT AREA DESIGNATION FLAG A INSTRUCTION FETCH BUS NONVOLATILE MEMORY USER REGION B USER REGION A DATA BUS REGION SWITCHING FLAG ADDRESS SIGNAL CPU

FIG. 5 (PRIOR ART)

